



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

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Serial No.: 09/114,504

Filed: HEREWITH

For: FLASH EEprom SYSTEM

San Francisco, California

Assistant Commissioner of Patents  
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Please preliminarily amend the above-identified new continuation patent application being filed herewith, as follows:

IN THE CLAIMS:

Please amend Claim 63 as follows:

63 (Amended). A flash memory card having a plurality of flash memory [devices] partitions each of which is divided into a plurality [or] of physical blocks, comprising:  
a connector for connecting said flash memory card to an external device;  
a data control logic circuit for controlling a transfer of data between the outside of said flash memory card and the plurality of flash memory [devices] partitions through said connector and respectively transmitting block erase commands to the flash memory [devices] partitions including the physical blocks to be erased when the block erase commands associated with a plurality of logic blocks are inputted via said connector; and  
an address control logic circuit for managing addresses for the plurality of logic blocks inputted via said connector so as to disperse into the plurality of flash memory [devices] partitions by assigning the addresses to their corresponding addresses for the physical blocks of the plurality of flash memory [devices] partitions and for respectively transmitting chip enable signals to at least

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